

VG-469

REFERENCE DESIGN #3

DRIVE BAY IMPLEMENTATION

ONE SLOT LOCAL
ONE SLOT REMOTE

OPTIONAL {
5V ISA VCC ONLY
EXTERNAL VOLTAGE SENSE
PLUG AND PLAY
NO DMA

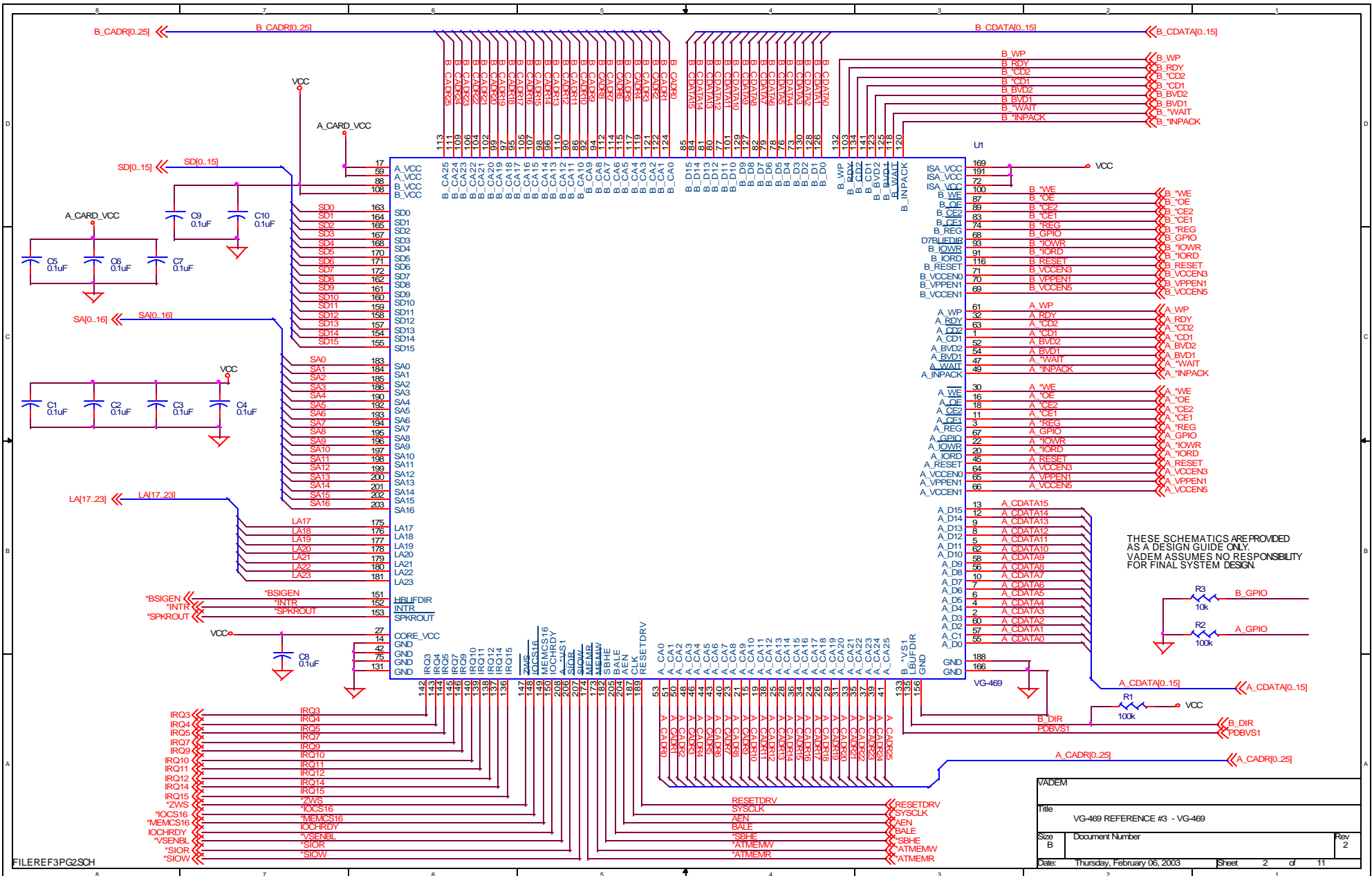
ISA BOARD

LINK
REF3PG2SCH
REF3PG3SCH
REF3PG4SCH
REF3PG5SCH
REF3PG6SCH
REF3PG7SCH
REF3PG8SCH
REF3PG9SCH
REF3PG10SCH
REF3PG11SCH

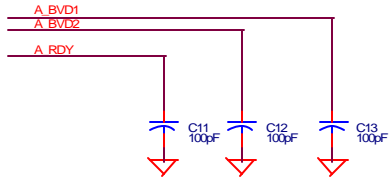
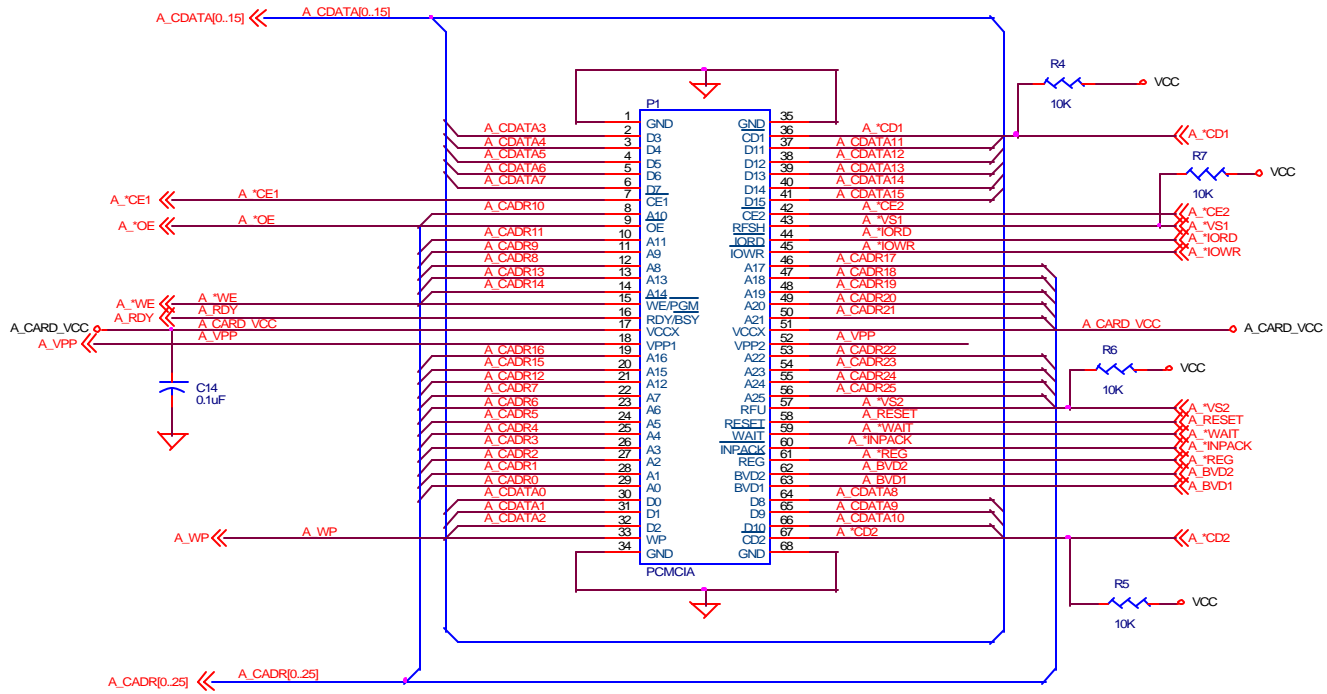
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FILE=469REF_3SCH

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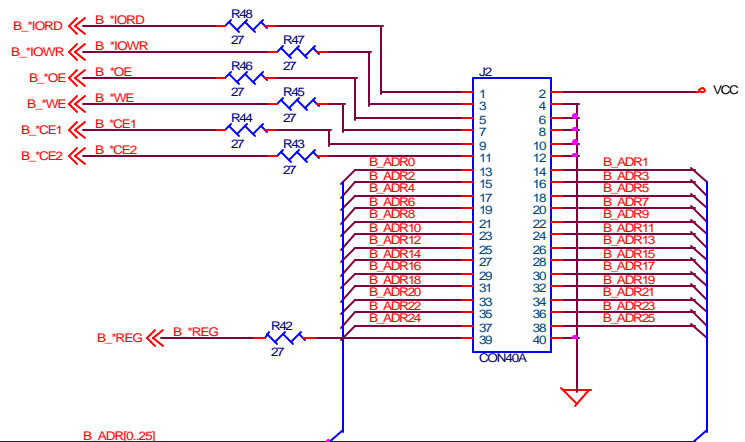
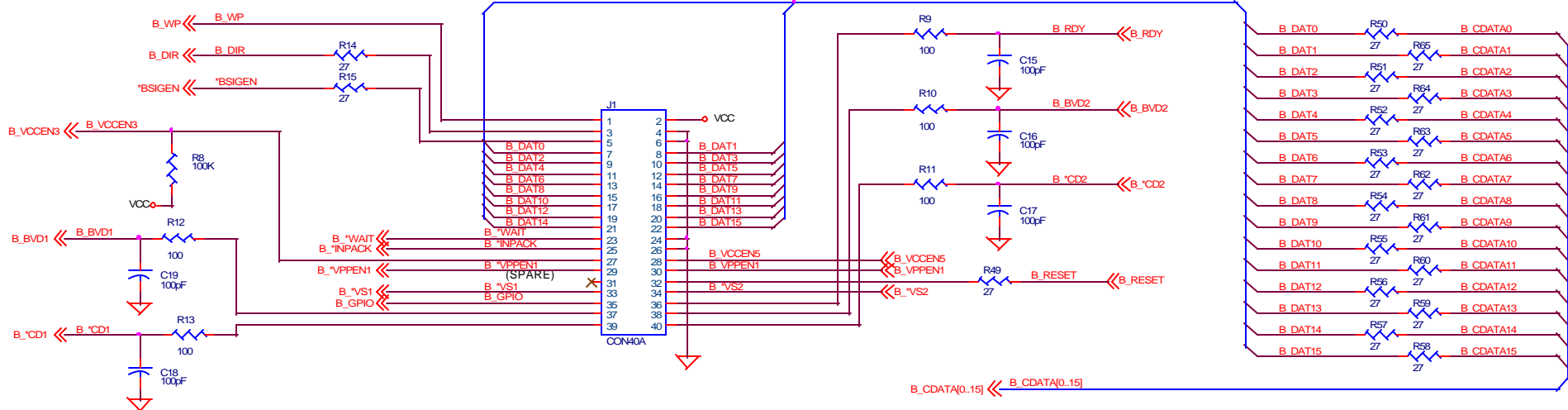
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FILE=REF3PG3SCH

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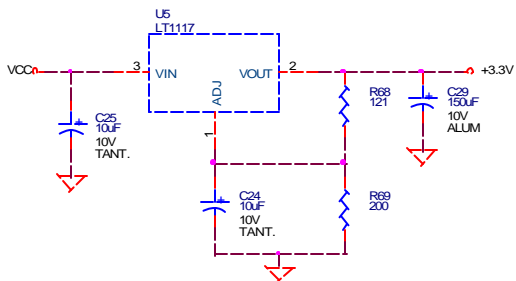
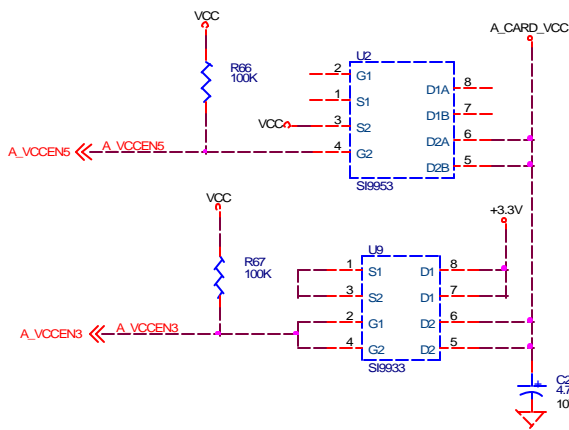
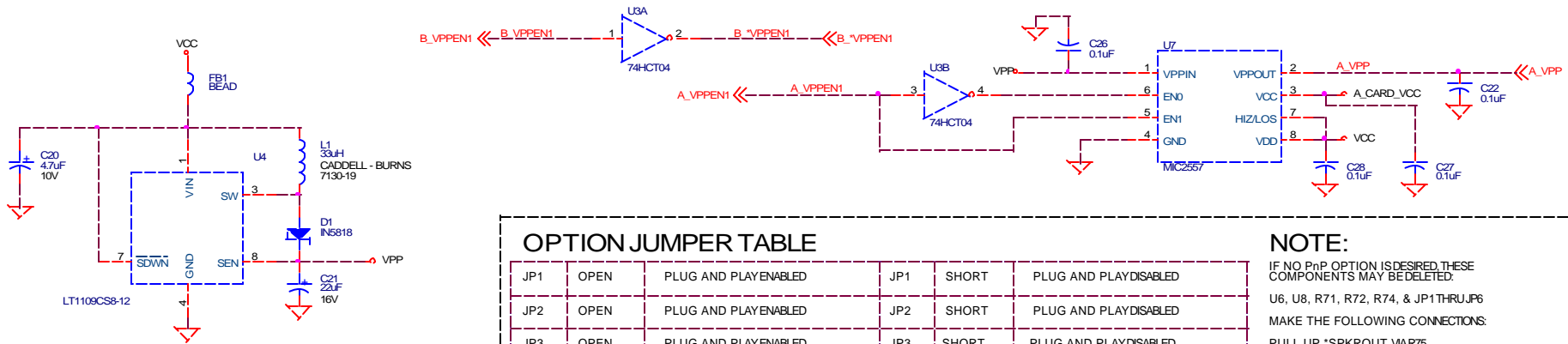
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B_ADR[0..25]

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FILE=REF3PG4SCH

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FILE=REF3PG6SCH

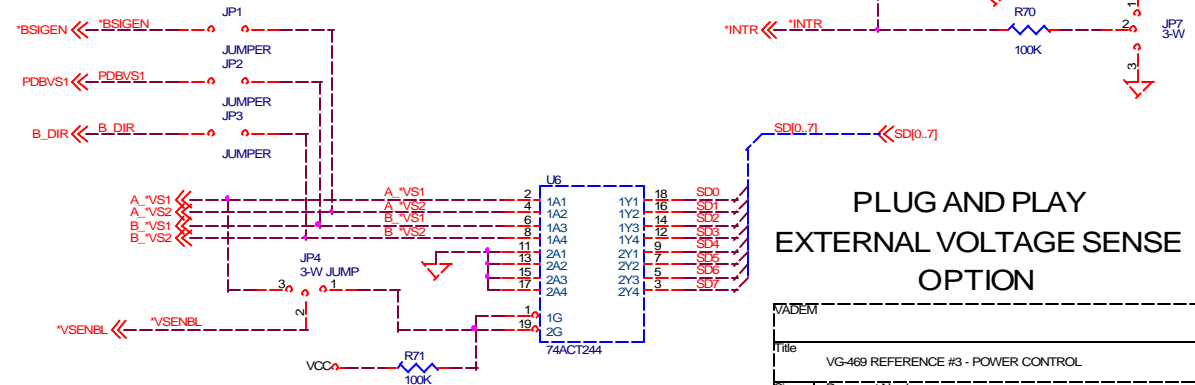
OPTION JUMPER TABLE

JP1	OPEN	PLUG AND PLAY ENABLED	JP1	SHORT	PLUG AND PLAY DISABLED
JP2	OPEN	PLUG AND PLAY ENABLED	JP2	SHORT	PLUG AND PLAY DISABLED
JP3	OPEN	PLUG AND PLAY ENABLED	JP3	SHORT	PLUG AND PLAY DISABLED
JP4	1-2	PLUG AND PLAY ENABLED	JP4	2-3	PLUG AND PLAY DISABLED
JP5	1-2	PLUG AND PLAY ENABLED	JP5	2-3	PLUG AND PLAY DISABLED
JP6	2-3	PLUG AND PLAY ENABLED	JP6	1-2	PLUG AND PLAY DISABLED

JP7	I/O ADDRESS
1-2	3E0/3E1H
2-3	3E2/3E3H

NOTE:

IF NO PnP OPTION IS DESIRED, THESE COMPONENTS MAY BE DELETED.
 U6, U8, R71, R72, R74, & JP1 THRU JP6
 MAKE THE FOLLOWING CONNECTIONS:
 PULL UP *SPKROUT VIA R75
 PULL UP PDBVS1 VIA R73
 CONNECT A-*VS1 TO *VSENBL
 CONNECT A-*VS2 TO *BSIGEN
 CONNECT B-*VS1 TO PDBVS1
 CONNECT B-*VS2 TO B_DIR



PLUG AND PLAY EXTERNAL VOLTAGE SENSE OPTION

VAD6M		
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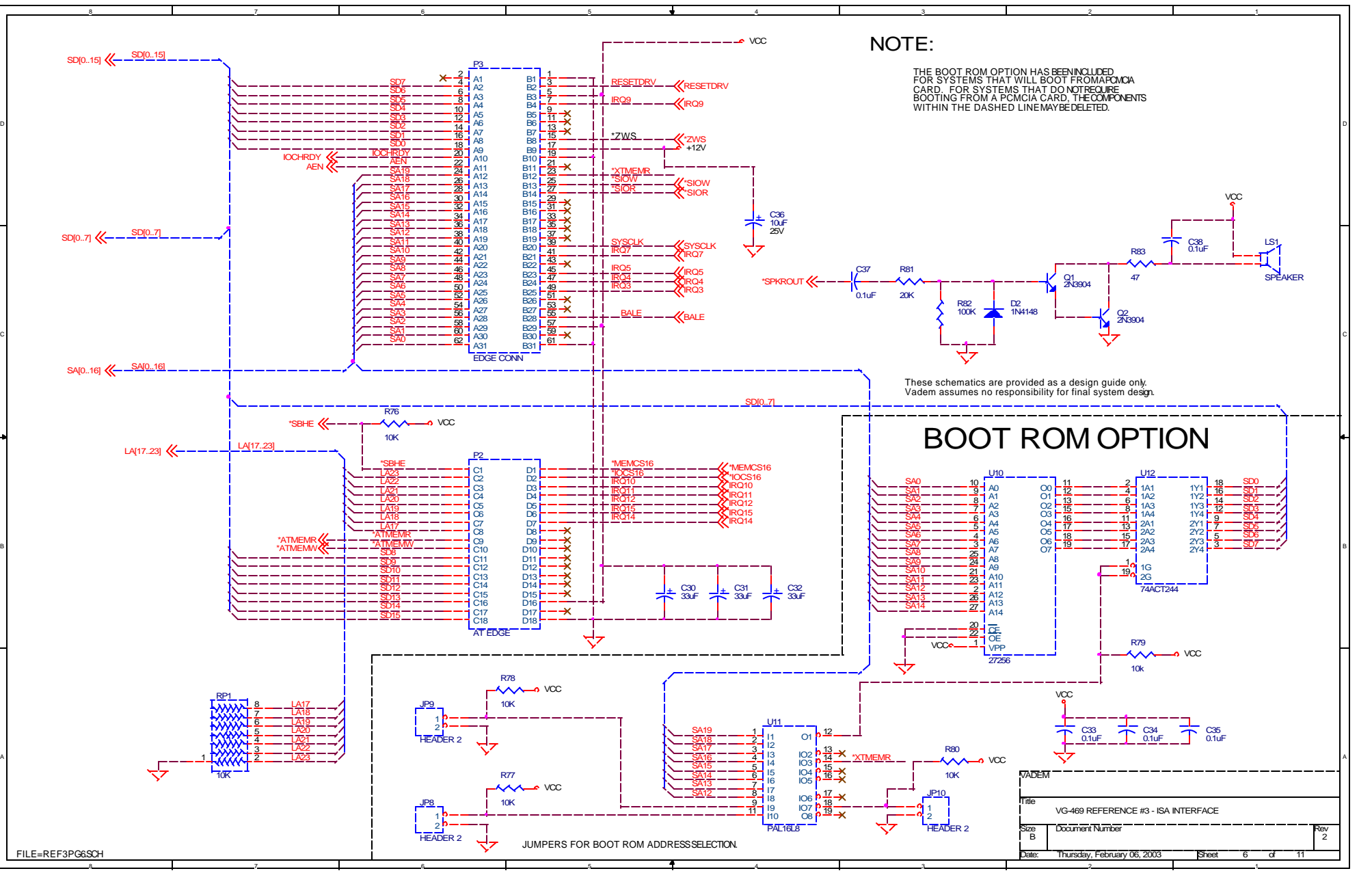
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NOTE:

THE BOOT ROM OPTION HAS BEEN INCLUDED FOR SYSTEMS THAT WILL BOOT FROM A PCMCIA CARD. FOR SYSTEMS THAT DO NOT REQUIRE BOOTING FROM A PCMCIA CARD, THE COMPONENTS WITHIN THE DASHED LINE MAY BE DELETED.

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BOOT ROM OPTION



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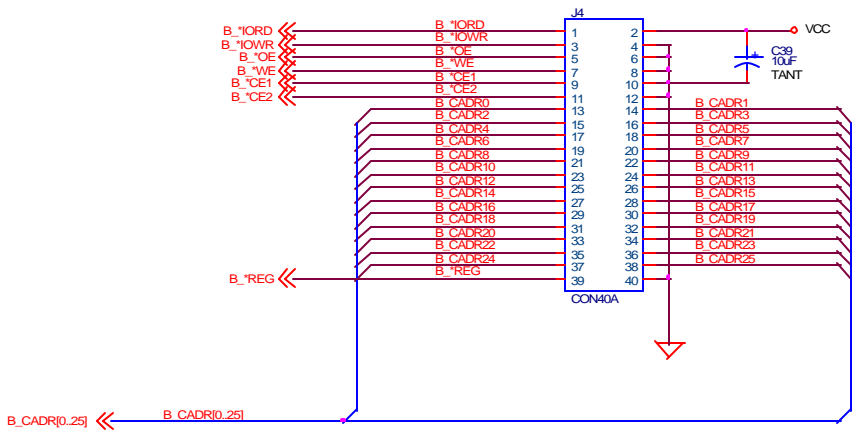
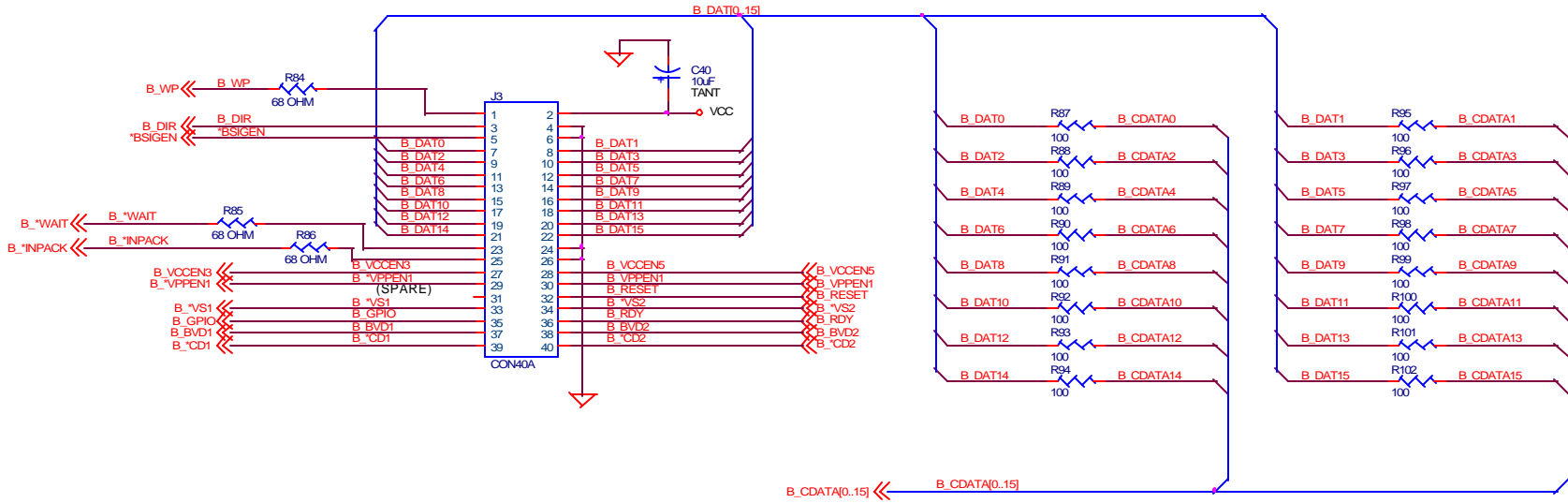
REFERENCE DESIGN #3

DRIVE BAY

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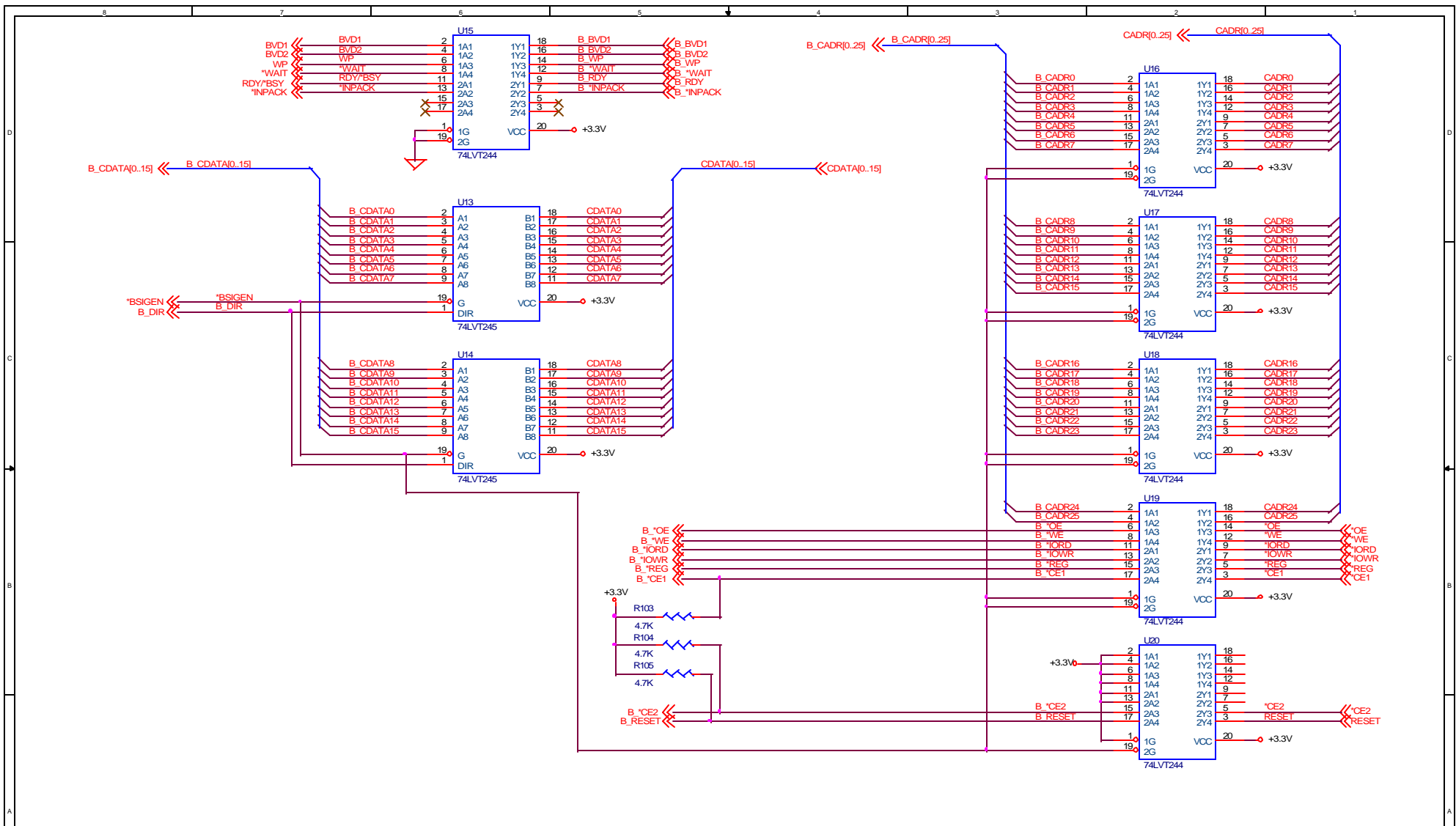
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VADEM		
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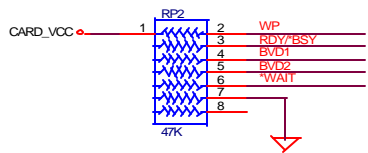
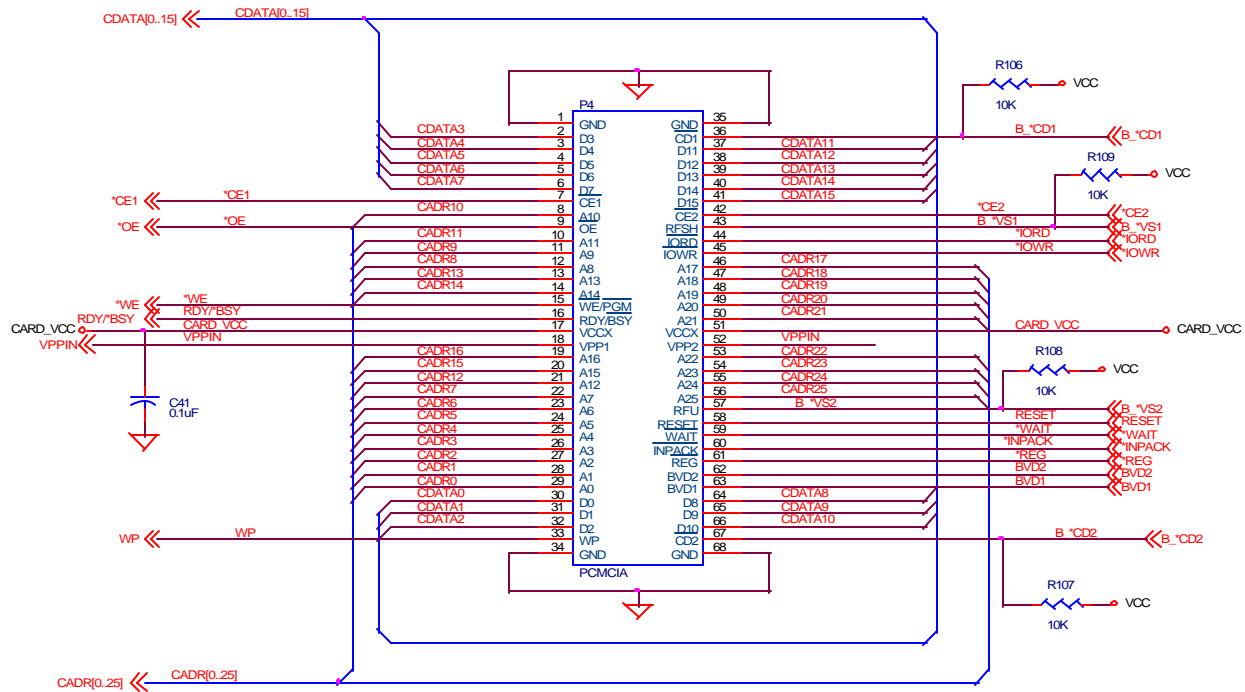
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Title: VG-469 REF #3 DRIVE BAY - CABLE CONNECTORS		
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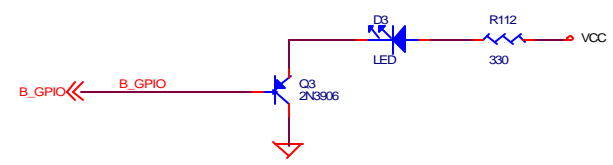
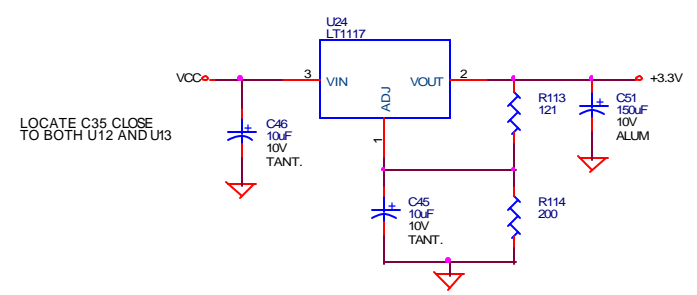
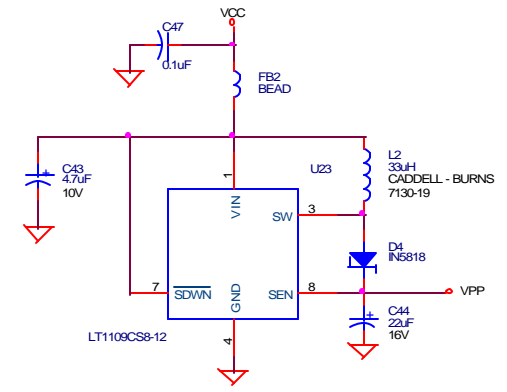
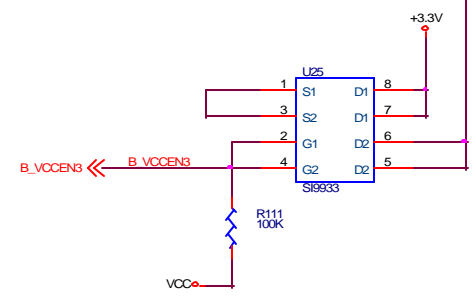
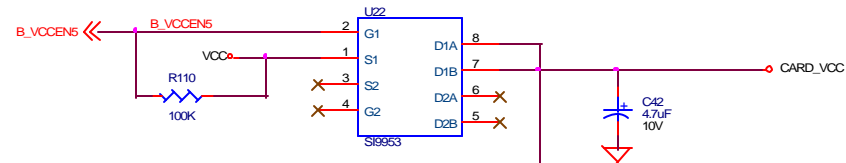
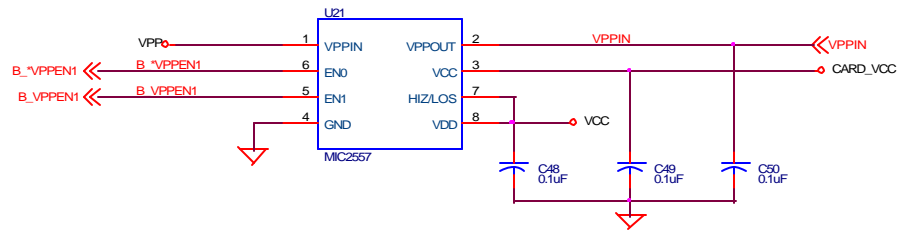
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LOCATE C35 CLOSE TO BOTH U12 AND U13

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