

VG-469 Configuration Table

Strapping Table

Registers are accessed through either 3E0 (*INTR strapped HIGH) or 3E2 (*INTR strapped LOW).
ISA can be operated at either 3.3V (B_GPIO strapped HIGH) or 5V (B_GPIO strapped LOW).

	B-1	B-2	B-3	E-1	E-2	E-3	E-4	E-5	E-6	E-7	E-8
*SPKROUT	H	H	H	L	L	L	L	L	L	L	L
A_VCCEN0 / B_VCCEN0	H/L or L/H	H/H or L/L	H/H or L/L	H/L or L/H	H/L or L/H	H/L or L/H	H/L or L/H	H/H or L/L	H/H or L/L	H/H or L/L	H/H or L/L
B_*VS1	-	-	-	H	H	L	L	H	H	L	L
B_*VS2	-	-	-	L	H	L	H	L	H	L	H

VG-469 Configuration Table

Basic Configurations (*SPKROUT strapped HIGH)

For all configurations in the following table, the registers can be accessed through either 3E0 or 3E2 (*INTR strapping option), ISA is operated at either 3.3V or 5V (B_GPIO strapping option)

Configurations		B-1	B-2	B-3
Functional Mode Options :				
Socket Voltage mode	5V ONLY	v	-	-
	Multi-Voltage (Internal Voltage Sense)	-	v	-
	Multi-Voltage (External Voltage Sense)	-	-	v
DMA mode	DMA	v	-	v
Signal Connection Options :				
DMA signals	HDRQ	B_GPIO	-	B_GPIO
	*HDACK	B_*VS1	-	B_*VS1
	HTC	A_GPIO		A_GPIO
External Voltage Sense signal	*VSENBL	-	-	A_*VS1
General Purposes Signals	*RIO	*INTR or IRQ15 (or A_GPIO or B_GPIO) ^A	*INTR or IRQ15 or A_GPIO or B_GPIO	*INTR or IRQ15 (or A_GPIO or B_GPIO) ^A
	*LED	IRQ12 (or A_GPIO or B_GPIO) ^A	IRQ12 or A_GPIO or B_GPIO	IRQ12 (or A_GPIO or B_GPIO) ^A
	A_VPPEN0	A_*VS2 or IRQ10 (or A_GPIO) ^A	A_*VS2 or IRQ10 or A_GPIO	A_*VS2 or IRQ10 (or A_GPIO) ^A
	B_VPPEN0	B_*VS2 or IRQ11 (or B_GPIO) ^A	B_*VS2 or IRQ11 or B_GPIO	B_*VS2 or IRQ11 (or B_GPIO) ^A

NOTE A: In configurations B-1 and B-3, signals allocated for DMA usage are available for GPIO usage if DMA function is not used and is disabled in the register.

VG-469 Configuration Table

Extended Configuration (*SPKROUT strapped LOW)

For all configurations in the following table, the registers can be accessed through either 3E0 or 3E2 (*INTR strapping option), ISA is operated at either 3.3V or 5V (B_GPIO strapping option)

Configurations		E-1	E-2	E-3	E-4	E-5	E-6	E-7	E-8
Functional Mode Options:									
Socket Voltages	5V ONLY mode	v	v	v	v	-	-	-	-
	Multi-Voltage (External VS) mode	-	-	-	-	v	v	v	v
Optional Functions	DMA ^C	v	v	-	-	v	v	-	-
	Plug & Play	-	-	v	v	-	-	v	v
Buffer Requirements	ISA ^B	v	-	v	-	v	-	v	-
	Socket	-	v	-	v	-	v	-	v
Signal Connection Options:									
General Purposes Signals	*RIO	*INTR or IRQ15 (or A_GPIO or B_GPIO) ^C	*INTR or IRQ15 (or A_GPIO or B_GPIO) ^C	IRQ15 (or A_GPIO or B_GPIO) ^C	IRQ15 (or A_GPIO or B_GPIO) ^C	*INTR or IRQ15 (or A_GPIO or B_GPIO) ^C	*INTR or IRQ15 (or A_GPIO or B_GPIO) ^C	IRQ15 (or A_GPIO or B_GPIO) ^C	IRQ15 (or A_GPIO or B_GPIO) ^C
	*LED	IRQ12 (or A_GPIO or B_GPIO) ^C	IRQ12 (or A_GPIO or B_GPIO) ^C	IRQ12 (or A_GPIO or B_GPIO) ^C	IRQ12 (or A_GPIO or B_GPIO) ^C	IRQ12 (or A_GPIO or B_GPIO) ^C	IRQ12 (or A_GPIO or B_GPIO) ^C	IRQ12 (or A_GPIO or B_GPIO) ^C	IRQ12 (or A_GPIO or B_GPIO) ^C
	A_VPPEN0	IRQ10 (or A_GPIO) ^C	IRQ10 (or A_GPIO) ^C	IRQ10 (or A_GPIO) ^C	IRQ10 (or A_GPIO) ^C	IRQ10 (or A_GPIO) ^C	IRQ10 (or A_GPIO) ^C	IRQ10 (or A_GPIO) ^C	IRQ10 (or A_GPIO) ^C
	B_VPPEN0	IRQ11 (or B_GPIO) ^C	IRQ11 (or B_GPIO) ^C	IRQ11 (or B_GPIO) ^C	IRQ11 (or B_GPIO) ^C	IRQ11 (or B_GPIO) ^C	IRQ11 (or B_GPIO) ^C	IRQ11 (or B_GPIO) ^C	IRQ11 (or B_GPIO) ^C

VG-469 Configuration Table

External Voltage Sense	*VSENBL	-	-	-	-	A_*VS1	A_*VS1	A_*VS1	A_*VS1
DMA signals	HDRQ	B_GPIO	B_GPIO	-	-	B_GPIO	B_GPIO	-	-
	*HDACK	B_*VS1	B_*VS1	-	-	B_*VS1	B_*VS1	-	-
	HTC	A_GPIO	A_GPIO	-	-	A_GPIO	A_GPIO	-	-
Plug & Play signals	E2CS	-	-	B_*VS1	B_*VS1	-	-	B_*VS1	B_*VS1
	E2SK	-	-	*INTR	*INTR	-	-	*INTR	*INTR
	E2DIO	-	-	*SPKROUT	*SPKROUT	-	-	*SPKROUT	*SPKROUT
ISA Buffer signals	HBUFDIR	A_*VS2	-	A_*VS2	-	A_*VS2	-	A_*VS2	-
	LBUFDIR	B_*VS2	-	B_*VS2	-	B_*VS2	-	B_*VS2	-
	D7BUFDIR	B_GPIO	-	B_GPIO	-	B_GPIO	-	B_GPIO	-
Socket Buffer signals	*BSIGEN	A_*VS2	-	A_*VS2	-	A_*VS2	-	A_*VS2	
	DIR	B_*VS2	-	B_*VS2	-	B_*VS2	-	B_*VS2	
Configurations		E-1	E-2	E-3	E-4	E-5	E-6	E-7	E-8

NOTE B: In configurations E-1 and E-5, DMA mode and ISA Buffer mode can not be used at the same time. ISA Buffer mode can be used if DMA function is disabled in the register.

NOTE C: In configurations E-1, E-2, E-5 and E-6, signals allocated for DMA usage can now be available as GPIO signals if DMA function is not used and is disabled in the register